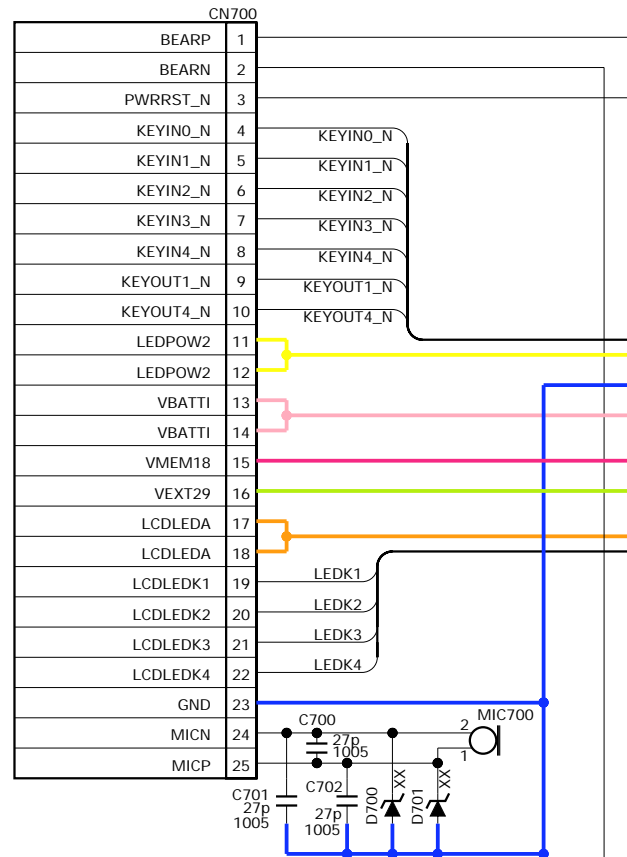
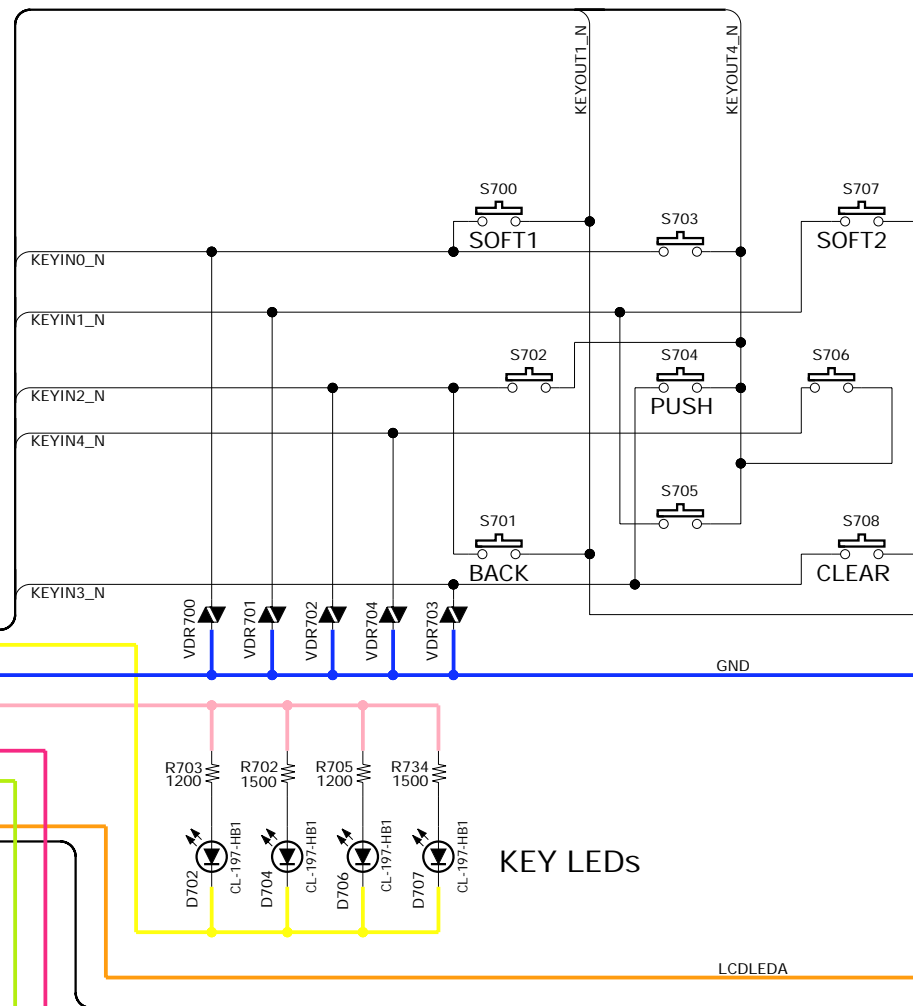
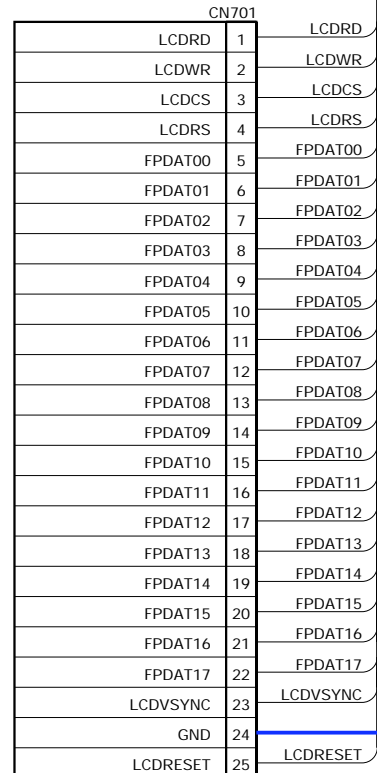


SONY STANDARD
'90.10

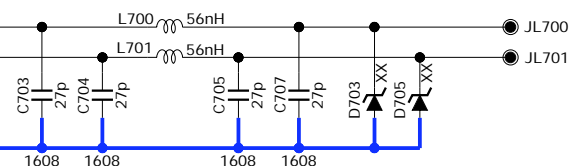
TO MAIN-PCB
I-PEX



I-PEX

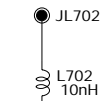


KEY LEDs

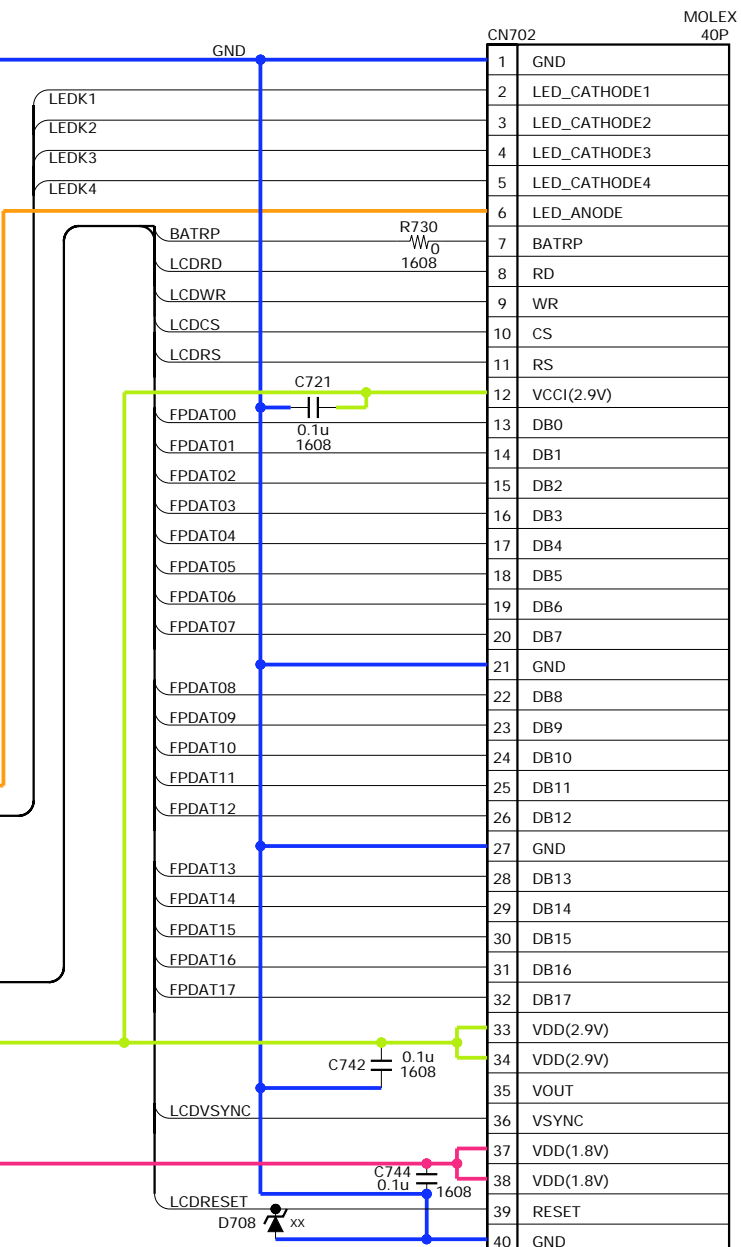


TO RECEIVER

TO PARASITIC ELEMENT



TO LCD-MODULE



CN702	1	GND	40P
	2	LED_CATHODE1	
	3	LED_CATHODE2	
	4	LED_CATHODE3	
	5	LED_CATHODE4	
	6	LED_ANODE	
	7	BATRP	
	8	RD	
	9	WR	
	10	CS	
	11	RS	
	12	VCCI(2.9V)	
	13	DB0	
	14	DB1	
	15	DB2	
	16	DB3	
	17	DB4	
	18	DB5	
	19	DB6	
	20	DB7	
	21	GND	
	22	DB8	
	23	DB9	
	24	DB10	
	25	DB11	
	26	DB12	
	27	GND	
	28	DB13	
	29	DB14	
	30	DB15	
	31	DB16	
	32	DB17	
	33	VDD(2.9V)	
	34	VDD(2.9V)	
	35	VOUT	
	36	VSYNC	
	37	VDD(1.8V)	
	38	VDD(1.8V)	
	39	RESET	
	40	GND	

\triangle_6 X		-----			UNIT mm	TOLERANCE	USED ON	RANK
\triangle_5 X		-----			ANGLE 			
\triangle_4 X		-----			SCALE :		ORIGINAL MODEL	
\triangle_3 X		-----			MATERIAL(COLOR)		FINISH(COLOR)	
\triangle_2 X		-----			DESCRIPTION (E)			
\triangle_1 X		-----						
HISTORY X COUNT	SUFFIX REPL.	ECN-NO. DATE	REVISION		SIGN.	(J)		
DRAWN BY SEMCJ GU・商品部 04.8.2 西村		PLANNED BY SEMCJ GU・商品部 04.8.2 青木		CHECKED BY SEMCJ GU・商品部 04.8.2 岡田	APPROVED BY SEMCJ GU・商品部 04.8.2 村田			
TENTATIVE						PART NO.		SHEET 1/1
						PART NO.		PTX-705 LCD_FLEX

Last Update : 2004/07/05

THIS DOCUMENT IS THE PROPRIETARY PROPERTY OF SONY. ITS USE IS AUTHORIZED ONLY FOR RESPONDING TO A REQUEST FOR QUOTATION, OR FOR THE PERFORMANCE OF WORK FOR SONY. ALL QUESTIONS MUST BE REFERRED TO THE LOCAL PURCHASING DEPARTMENT OF SONY.